

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	25	turbo adj code and BCH and "Reed-Solomon"	USPAT	OR	OFF	2006/03/31 14:08
S1	990	frame adj circuit	USPAT	OR	OFF	2006/03/28 16:56
S2	8	frame adj circuit\$1 and pld	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	ON	2006/03/28 17:28
S3	14546	pld	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	ON	2006/03/28 16:56
S4	4994	pld\$1 and FPGA	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	ON	2006/03/28 17:28
S6	227	714/725.ccls.	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2006/03/29 10:44
S7	0	714/725.ccls and check adj word\$1	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2006/03/29 10:44
S8	0	714/725.ccls and comparator	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2006/03/29 10:44
S9	0	714/725.ccls and compar\$4	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2006/03/29 10:44

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S10	0	714/725.ccls. and check adj work\$1	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2006/03/29 10:45
S11	0	714/725.ccls. and check adj word\$1	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2006/03/29 10:45
S12	34	714/725.ccls. and comparator.	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2006/03/30 10:58
S13	9	714/725.ccls. and (comparator and pld)	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2006/03/29 10:45
S14	13	714/725.ccls. and parity	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2006/03/30 11:26
S15	2	714/725.ccls. and parity and buffer	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2006/03/30 11:47
S16	21	714/725.ccls. and mask	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2006/03/30 12:28
S17	0	714/725.ccls. and mask adj memory	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2006/03/30 11:47

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S18	2	714/766.ccls. and pld	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2006/03/30 12:44
S19	59	714/766.ccls. and test\$3	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2006/03/30 13:57
S20	143150	714/766.ccls. and pld or pla	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2006/03/30 14:07
S21	4	714/766.ccls. and (pld or pla)	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2006/03/30 14:01
S22	32	"6102963"	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2006/03/30 14:04
S23	20	"650030"	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2006/03/30 14:04
S24	7	"6550030"	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2006/03/30 14:04
S25	8	714/782-785.ccls. and (pld or pla)	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2006/03/30 14:20

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S26	20	714/758-759.ccls. and (pld or pla)	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2006/03/30 15:10
S27	871	326/38-39.ccls. and (pld or pla)	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2006/03/30 15:11
S28	354	326/38-39.ccls. and ((pld or pla) and test\$3)	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2006/03/30 15:12
S29	22	326/38-39.ccls. and ((pld or pla) and test\$3 and parity)	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2006/03/30 15:19
S30	214	326/38-39.ccls. and ((pld or pla) and test\$3 and buffer)	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2006/03/30 15:20
S31	28	326/38-39.ccls. and ((pld or pla) and test\$3 and buffer and check)	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2006/03/30 15:50
S32	45	716/3.ccls. and (pld or pla)	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2006/03/30 17:00
S33	29	716/3.ccls. and (pld)	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2006/03/30 15:51

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S34	29	716/3.ccls. and (pld or pla and testing)	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2006/03/30 17:02
S35	12	(716/3.ccls. and (pld or pla) and testing)	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2006/03/30 17:04
S36	157	(716/16-19.ccls. and (pld or pla) and testing)	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2006/03/30 17:04
S37	134	(716/16-19.ccls. and (pld) and testing)	US-PGPUB; USPAT; USOCR; EPO; DERWENT; IBM_TDB	OR	OFF	2006/03/30 17:04
S38	134	(716/16-19.ccls. and (pld) and testing)	US-PGPUB; USPAT	OR	OFF	2006/03/30 17:04
S39	134	716/16-19.ccls. and pld and testing	US-PGPUB; USPAT	OR	OFF	2006/03/30 17:05
S40	21	716/16-19.ccls. and pld with testing	US-PGPUB; USPAT	OR	OFF	2006/03/30 17:14
S41	0	716/41.ccls. and pld with testing	US-PGPUB; USPAT	OR	OFF	2006/03/30 17:14
S42	0	716/41.ccls. and pld	US-PGPUB; USPAT	OR	OFF	2006/03/30 17:14
S43	12216	716/41.ccls. and pld or pla	US-PGPUB; USPAT	OR	OFF	2006/03/30 17:14
S44	12216	716/41.ccls. and pld or pla	US-PGPUB; USPAT	OR	OFF	2006/03/30 17:14
S45	0	716/41.ccls. and (pld or pla)	US-PGPUB; USPAT	OR	OFF	2006/03/30 17:14
S46	148	pld with testing	US-PGPUB; USPAT	OR	OFF	2006/03/30 19:21
S47	58	pld with testing and buffer	US-PGPUB; USPAT	OR	OFF	2006/03/30 17:14
S48	50	(pld with testing) and (parity or check)	US-PGPUB; USPAT	OR	OFF	2006/03/30 17:26

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S49	93	pld with testing not Xilinx	US-PGPUB; USPAT	OR	OFF	2006/03/30 19:24
S50	50	(pld with testing) and (parity or check)	US-PGPUB; USPAT	OR	OFF	2006/03/30 19:25
S51	69	S49 not S50	US-PGPUB; USPAT	OR	OFF	2006/03/30 19:25
S52	9	(US-6304101-\$ or US-6574761-\$ or US-4380811-\$ or US-6694464-\$ or US-6839873-\$ or US-6938193-\$ or US-6701480-\$ or US-6216248-\$ or US-5943488-\$).did.	USPAT	OR	OFF	2006/03/31 10:45
S53	2	S52 and frame	USPAT	OR	OFF	2006/03/31 10:49
S54	2	S52 and frame and stor\$3	USPAT	OR	OFF	2006/03/31 11:21
S55	1	S52 and frame and stor\$3 and buffer	USPAT	OR	OFF	2006/03/31 12:12
S56	2	S52 and mask	USPAT	OR	OFF	2006/03/31 12:12
S57	2	S52 and mask\$3	USPAT	OR	OFF	2006/03/31 14:08